



Mainframe Technology Today

Friday, April 18th, 2008
in Berlin Adlershof

IBM System z System & Processor Design

Agenda

2:00PM – 5:00 PM

IBM System z10 –The Next-Generation
Mainframe Microprocessor

Balanced Design for Mainframe
Processors & Systems

Question & Answer



Charles F. Webb

IBM Fellow,
Chief System z Design Architect

IBM System z10, the latest in the long line of IBM mainframe computer systems, boasts a 4.4 GHz quad-core microprocessor, on-chip accelerators, and industry-leading virtualization capabilities. This seminar will address various aspects of microprocessor and system design on the System z10.

Charles F. Webb has been designing mainframe processors since 1983 and was the chief engineer for the System z10 processor. In 2003 he was named an IBM Fellow, the highest technical honor within IBM.

Location:

Room 0'119 (next to the cafe)
Erwin-Schrödinger-Zentrum
Rudower Chaussee 26
12489 Berlin-Adlershof

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